VERIFICATION OF TRUTH TABLES OF LOGIC GATES USING UNIVERSAL GATES (NAND & NOR GATES) - GUPTA BRAND 200377



Objective : To verify the truth table of AND gate, OR gate, NOR gate, NAND gate & NOT gate using NAND & NOR gate.
Features : Instrument comprises of DC Regulated Power Supply 5V/150mA, 2 logic '1' & 2 logic '0' inputs are provided on sockets, 2 Red LED output indicators, circuit diagram printed for 4 'NAND' & 4 'NOR' gates & their respective IC's placed inside the cabinet & connections brought out at sockets

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