TTL-CMOS & CMOS -TTL INTERFACING - GUPTA BRAND 200394

PRODUCT BROCHURE ON REQUEST

Objective : To Study the interfacing of TTL-CMOS and CMOS-TTL using pull up resistor.
Features : Instrument comprises of Two DC Regulated Power Supplies of 5V/150mA, 12V/150mA, output indicator for CMOS & TTL individually. Built in pull up network of resistor. IC\'s are mounted on front panel & appropriate test point are provided on front panel. One CMOS & One TTL IC are provided for experiment.

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